

6

Docket No.: P-0309

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Application of

: **OFFICIAL DRAFTSMAN**

:

Hyun Duk CHO and Sung Deuk KIM

:

Serial No.: 10/002,183

: Group Art Unit: 2631

:

Confirm. No.: 3476

: Examiner: Unassigned

:

Filed: December 5, 2001

:

For: **METHOD FOR CODING MOTION VECTOR USING 2-DIMENSIONAL  
MINIMUM BITRATE PREDICTING TECHNIQUE**

:

**LETTER SUBMITTING FORMAL DRAWINGS**

Assistant Commissioner for Patents  
Washington, D. C. 20231

Sir:

Submitted herewith are three (3) sheets of formal drawings (Figures 1-9) in connection  
with the above-identified application.

Respectfully submitted,  
FLESHNER & KIM, LLP

Daniel Y.J. Kim  
Registration No. 36,186

P. O. Box 221200  
Chantilly, VA 20153-1200  
703 502-9440 DYK/cah  
**Date:** February 26, 2002

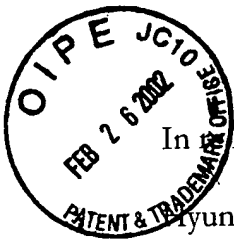


FIG. 1  
BACKGROUND ART

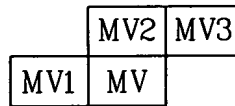


FIG. 2  
BACKGROUND ART

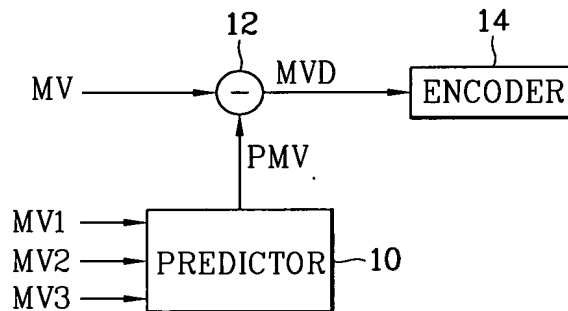


FIG. 3A  
BACKGROUND ART

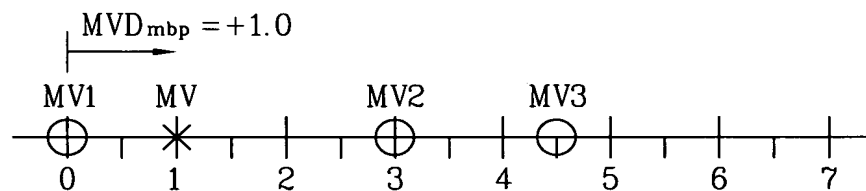


FIG. 3B  
BACKGROUND ART

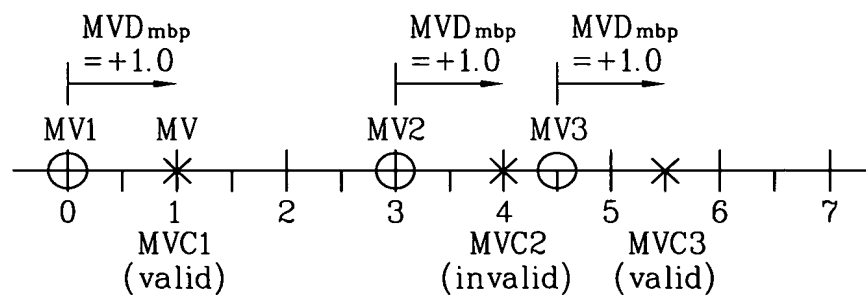




FIG. 4

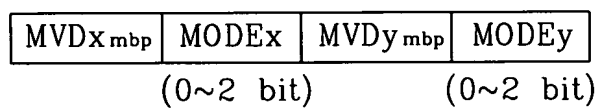


FIG. 5

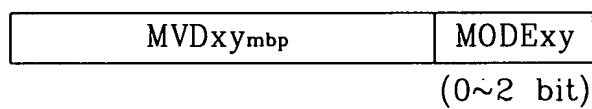


FIG. 6

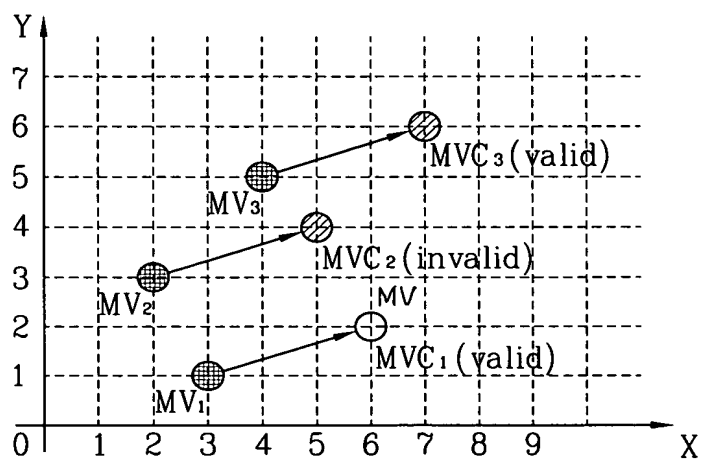




FIG. 7

No. of valid MVCi	True motion vector among valid MVCi			No. of bits	Code
1	Unique			0	x
2	MVC1	MVC1	MVC2	1	"0"
	MVC2	MVC3	MVC3	1	"1"
3		MVC1		1	"0"
		MVC2		2	"10"
		MVC3		2	"11"

FIG. 8

Syntax(Code)	Meaning
"1"	MVDx=0 and MVDy=0
"00"+MVDx+MVDy	MVDx≠0 and MVDy≠0
"010"+MVDx	MVDx≠0 and MVDy=0
"011"+MVDy	MVDx=0 and MVDy≠0

FIG. 9

MVDx <sub>mbp</sub> + MVDy <sub>mbp</sub>	MODEx + MODEy
(0~4 bit)	